



The EP AMC DEV board is a standalone development board for advanced mezzanine cards (AMC). It is an AMC carrier primarily intended for testing and verification of ports 0-11 on AMC module designs and for software development using AMC modules. The EP board operates as a stand-alone carrier powered from an ATX power supply and implements full hot swap support and also incorporates an IPMI controller (IPMC) based on the Pigeon Point™ BMR-H8S-ATCA reference design.

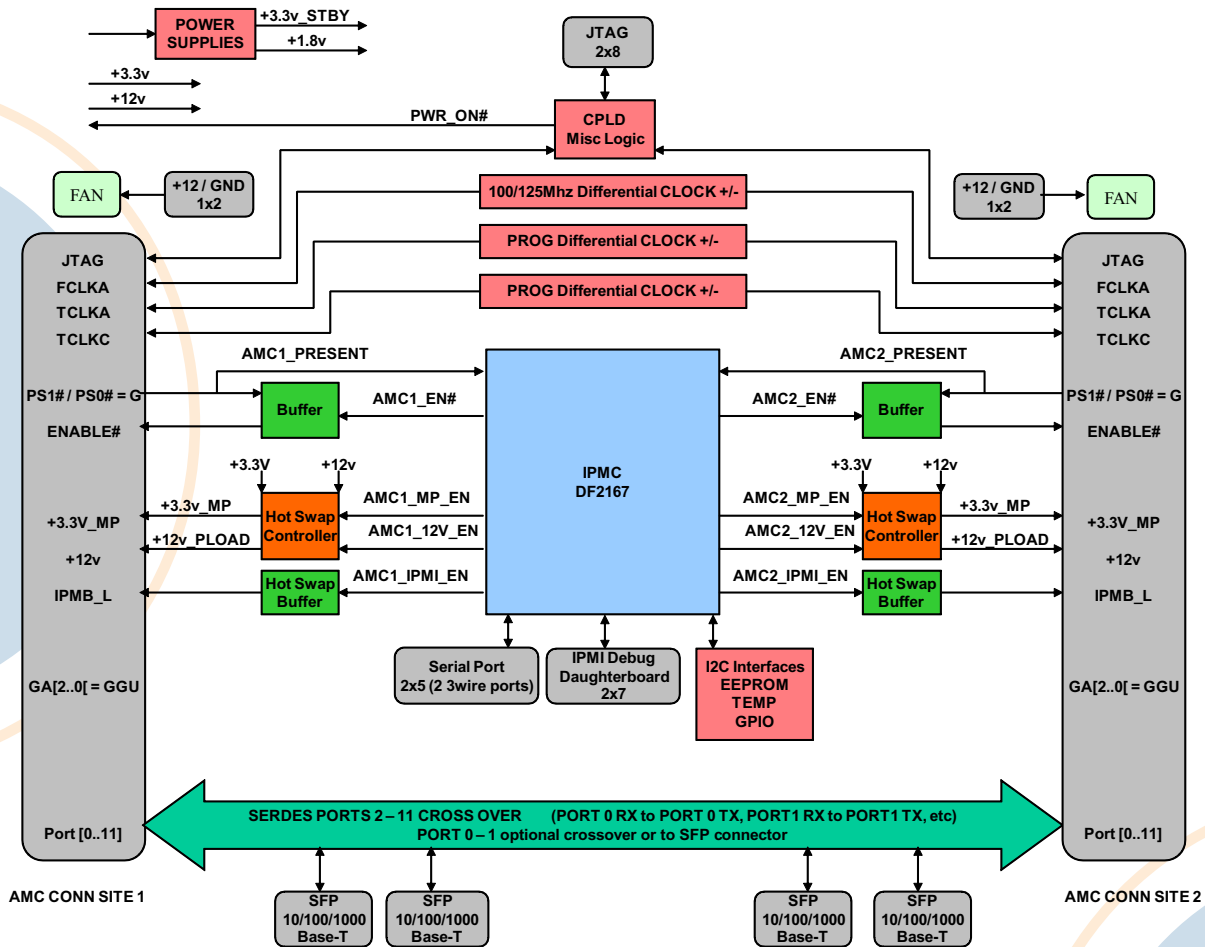
The EP AMC DEV board has direct connectivity for AMC ports 0-11 between the 2 AMC slots and was designed to simulate the maximum trace path for MicroTCA™ and ATCA™ systems, with a port to port length of 282mm for verification of 5G rates as in an actual system environment.

The EP AMC DEV board has full access to all SERDES ports 0-11 signals via the on board Mid-Bus Probe land patterns allowing easy connections to logic analyzers and protocol test equipment. Additional features include the ability to route AMC ports 0-1 for each AMC slot to the rear SFP+ ports. Onboard fans provide direct air flow for each AMC slot.

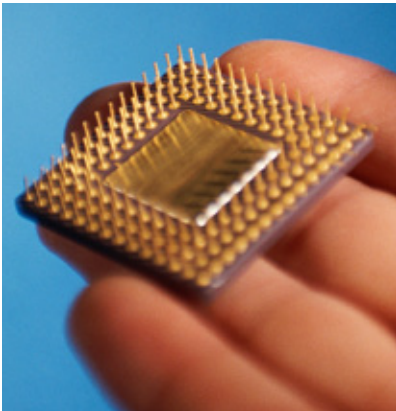
## Feature Summary:

Specification:	Description:
Form Factor:	AMC cutaway carrier AMC.0 (2 slots operational)
Power Supply:	Standard ATX with minimum requirements; +12v@ 17A, +5v_STBY@ 500ma, +3.3v@ 3A
Operating temp:	0-55C
Cooling:	2 fans per AMC slot, 4.2 CFM per fan.
IPMI:	Renesas_H8S2167 Microcontroller. Based on Pigeon Point™ BMR-H8S-ATCA reference design. Serial ports, 2x5 header. Optional debug header, 2x7 header.
FCLK clock generator:	100Mhz or 125Mhz, jumper selectable. LVDS or HSCL, jumper selectable.
TCLKA clock generator:	Build specific options for frequency and format. Default is 19.44Mhz, LVDS. Contact EP for other options.
TCLKC clock generator:	Build specific options for frequency and format. Default is 8.0Khz, LVDS. Contact EP for other options.
HOT SWAP controller:	+3.3v@150ma management power per AMC slot. +12v@7.5A Payload power per AMC slot.
MID-BUS Probe land patterns:	3 MID-BUS probe land patterns for complete AMC port 0-11 access.
SFP+ connectors:	Jumper selectable connections for AMC ports 0-1 to the other AMC slot or to the rear access SFP+ connectors allowing for customer specific SFP modules.
Power / Reset button:	On board power on / off / reset button. 1 x 2 header allows for remote access to this button.
SERDES:	Routed length of 282mm between AMC slots to simulate actual 5G system.

## Hardware Block Diagram



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- Embedded Planet has extensive experience with embedded operating systems and firmware. Our stock configurations of operating systems and firmware can be customized to meet your particular needs.
- We can alleviate the headaches associated with volume production of embedded systems. Your product is delivered 100% tested from an ISO-9002 certified manufacturing facility.
- Our capabilities are available on a project basis to design custom solutions specifically tailored to your application.
- Contact Embedded Planet to find out how we can accelerate your project.