



The EP3041A is a single width, full height AMC processor board featuring the high performance, next generation Freescale QorIQ P3041 with quad core e500mc PowerPC processors operating at up to 1.5GHz.

The highly integrated multi-core processor makes the EP3041A a natural fit for combined control and data plane applications on a single board, thereby reducing system cost, thermal budget and power consumption. The EP3041A is built for a range of computationally intensive applications in wireless base stations, media gateways, enterprise network access systems, and test and measurement systems. The built-in RAID 5/6 and Data Path accelerators allow for high-reliability storage area networks (SAN), server blades and network attached storage (NAS) that feature Policy Control and Deep Packet Inspection (DPI).

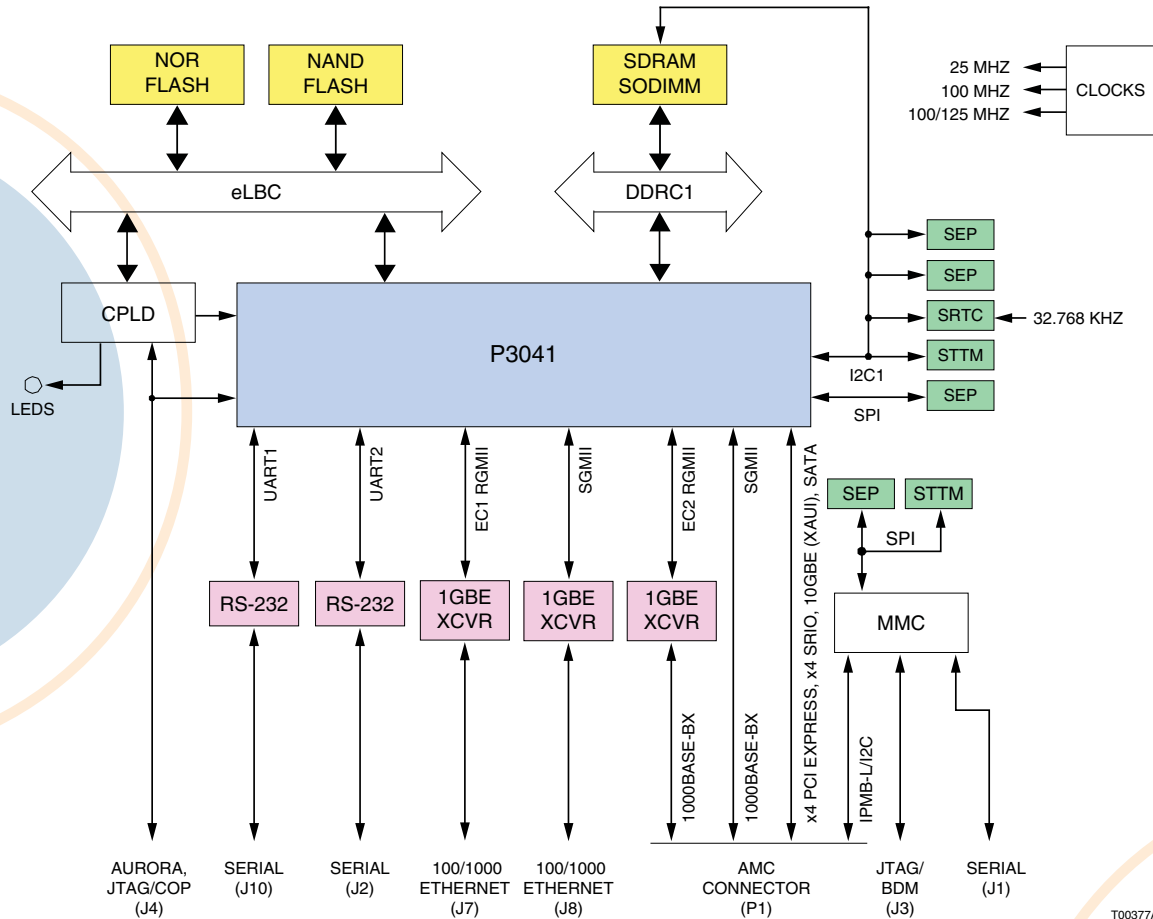
## Feature Summary:

Specification:	Description:
Form Factor:	Full or Mid-Height AMC.0
Processor:	Freescale Quad-Core QorIQ P3041 at up to 1.5GHz
Backplane Options:	PCIe version 2.0 up to 5Gbps, SRIO version 1.3 with features of version 2.1 up to 5Gbps. RapidIO message manager (RMan) with Type 5-6 and Type 8-11 support. XAUI 10Gbps Ethernet controller (1 port available). SGMII, Ethernet Controller (five 1Gbps or four 2.5Gbps available).
Front Panel:	2 x GbE connections and 1 x RS-232 serial
Ethernet:	Up to four GigE ports configurable between the front panel and AMC channels 0 and 1
Memory:	Up to 4 GB DDR3 with ECC in one SODIMM, up to 128 MB of NOR flash and 1GB of NAND flash
SATA:	SATA version 2.0, supporting 1.5 and 3.0Gbps
Debug:	JTAG/COP via an onboard connector
Power Supply:	Via AMC connector per AMC.0 or from barrel connector
Software:	U-Boot bootloader, Linux, INTEGRITY, and VxWorks BSP available

For fabric connectivity the EP3041A offers extreme flexibility, while maintaining standards compliance. All versions of the board support SGMII on channel 0, compliant with AMC.2, Type E2. Channels 2 and 3 support SATA. The fat pipe region of the AMC (channels 4 - 11) can be configured on a build basis to support combinations of 10G Ethernet XAUI, Gen 2 PCI express, and SRIO 1.3/2.1 controllers.



### Hardware Block Diagram



T00377A

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