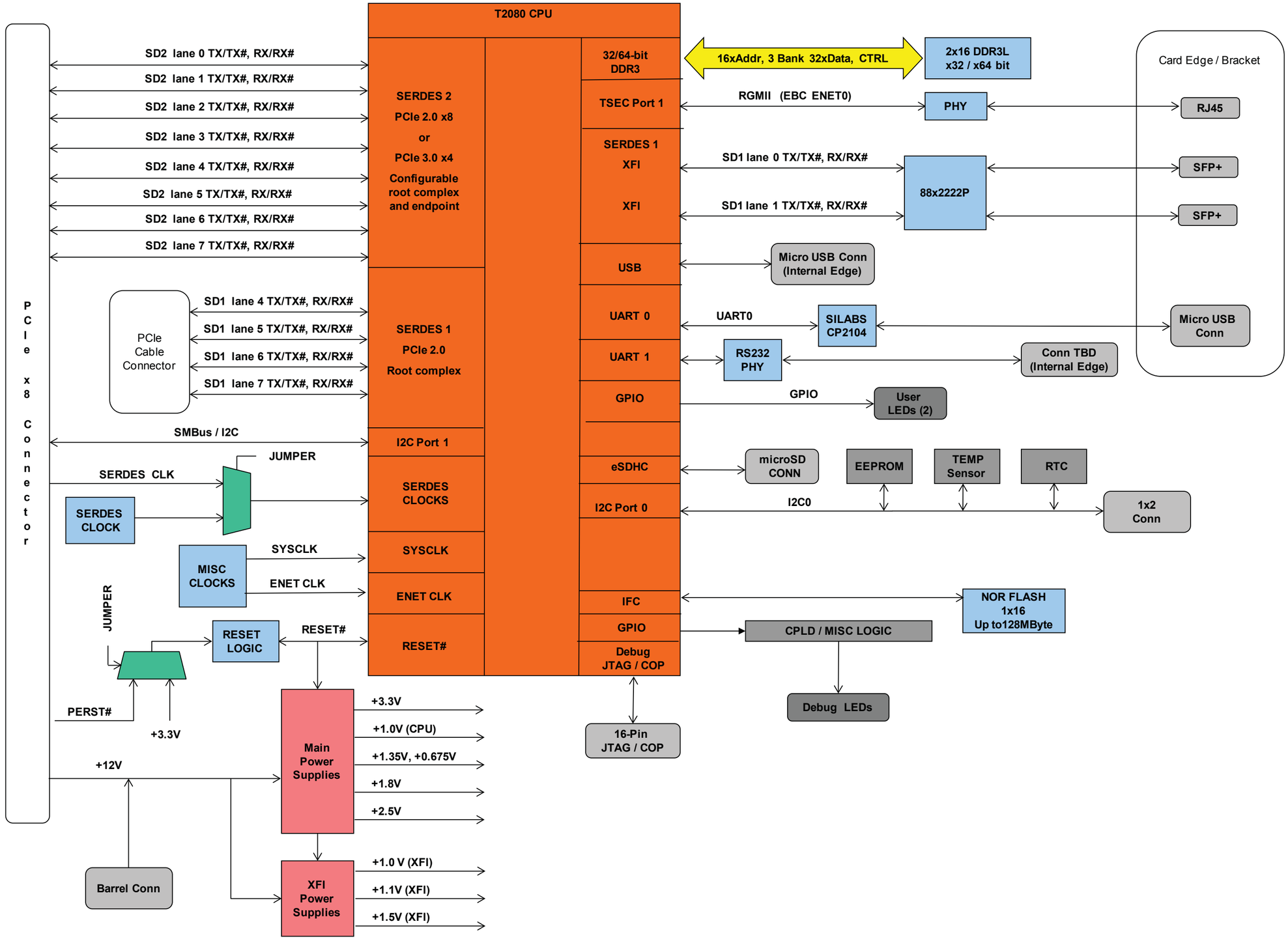
T2080 CPU
PCIe x8 Connector
SD2 lane 0 TX/TX#, RX/RX#
SD2 lane 1 TX/TX#, RX/RX#
SD2 lane 2 TX/TX#, RX/RX#
SD2 lane 3 TX/TX#, RX/RX#
SD2 lane 4 TX/TX#, RX/RX#
SD2 lane 5 TX/TX#, RX/RX#
SD2 lane 6 TX/TX#, RX/RX#
SD2 lane 7 TX/TX#, RX/RX#
SERDES 2
PCIe 2.0 x8
or
PCIe 3.0 x4
Configurable root complex and endpoint
32/64-bit DDR3
16xAddr, 3 Bank 32xData, CTRL
2x16 DDR3L x32 / x64 bit
Card Edge / Bracket
TSEC Port 1
RGMII (EBC ENET0)
PHY
RJ45
SERDES 1
XFI
XFI
SD1 lane 0 TX/TX#, RX/RX#
SD1 lane 1 TX/TX#, RX/RX#
88x2222P
SFP+
SFP+
USB
Micro USB Conn (Internal Edge)
PCIe Cable Connector
SD1 lane 4 TX/TX#, RX/RX#
SD1 lane 5 TX/TX#, RX/RX#
SD1 lane 6 TX/TX#, RX/RX#
SD1 lane 7 TX/TX#, RX/RX#
SERDES 1
PCIe 2.0
Root complex
UART 0
UART0
SILABS CP2104
Micro USB Conn
UART 1
RS232 PHY
Conn TBD (Internal Edge)
GPIO
GPIO
User LEDs (2)
SMBus / I2C
I2C Port 1
JUMPER
SERDES CLK
SERDES CLOCKS
SERDES CLOCK
eSDHC
microSD CONN
EEPROM
TEMP Sensor
RTC
I2C Port 0
I2C0
1x2 Conn
SYSCLK
SYSCLK
MISC CLOCKS
ENET CLK
ENET CLK
JUMPER
IFC
NOR FLASH 1x16 Up to128MByte
GPIO
CPLD / MISC LOGIC
RESET LOGIC
RESET#
RESET#
Debug JTAG / COP
Debug LEDs
PERST#
+3.3V
16-Pin JTAG / COP
+3.3V
+1.0V (CPU)
+1.35V, +0.675V
+1.8V
+2.5V
Main Power Supplies
+12V
Barrel Conn
XFI Power Supplies
+1.0 V (XFI)
+1.1V (XFI)
+1.5V (XFI)